

Power Analyser UMG 806

Modbus address list
(Firmware Version 2.026 and higher)



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Address list UMG 806

Frequently required readings

Address	Format	RD/WR	Designation	Unit	Note
19000	float	RD	_G_ULN[0]	V	Voltage L1-N
19002	float	RD	_G_ULN[1]	V	Voltage L2-N
19004	float	RD	_G_ULN[2]	V	Voltage L3-N
19006	float	RD	_G_ULL[0]	V	Voltage L1_L2
19008	float	RD	_G_ULL[1]	V	Voltage L2_L3
19010	float	RD	_G_ULL[2]	V	Voltage L3_L1
19012	float	RD	_G_ILN[0]	A	Apparent current,L1-N
19014	float	RD	_G_ILN[1]	A	Apparent current,L2-N
19016	float	RD	_G_ILN[2]	A	Apparent current,L3-N
19018	float	RD	_G_ILN[3]	A	Apparent current, L4-N
19020	float	RD	_G_PLN[0]	W	Real power L1-N
19022	float	RD	_G_PLN[1]	W	Real power L2-N
19024	float	RD	_G_PLN[2]	W	Real power L3-N
19026	float	RD	_G_P_SUM3	W	Psum3=P1+P2+P3
19028	float	RD	_G_SLN[0]	VA	Apparent power L1-N
19030	float	RD	_G_SLN[1]	VA	Apparent power L2-N
19032	float	RD	_G_SLN[2]	VA	Apparent power L3-N
19034	float	RD	_G_S_SUM3	VA	Sum; Ssum=S1+S2+S3
19036	float	RD	_G_QLN[0]	var	Reactive power L1 (fundamental comp.)
19038	float	RD	_G_QLN[1]	var	Reactive power L2 (fundamental comp.)
19040	float	RD	_G_QLN[2]	var	Reactive power L3 (fundamental comp.)
19042	float	RD	_G_Q_SUM3	var	Qsum3=Q1+Q2+Q3
19044	float	RD	_G_COS_PH[0]	-	CosPhi; UL1 IL1 (fundamental comp.)
19046	float	RD	_G_COS_PH[1]	-	CosPhi; UL1 IL2 (fundamental comp.)
19048	float	RD	_G_COS_PH[2]	-	CosPhi; UL1 IL3 (fundamental comp.)
19050	float	RD	_G_FREQ	Hz	Measure frequency
19052	float	RD	-	-	Reserve
19054	float	RD	_G_WH[0]	Wh	Real energy L1
19056	float	RD	_G_WH[1]	Wh	Real energy L2
19058	float	RD	_G_WH[2]	Wh	Real energy L3
19060	float	RD	_G_WH_SUML13	Wh	Real energy L1..L3
19062	float	RD/WR	_G_WH_V[0]	Wh	Real energy L1, consumed
19064	float	RD/WR	_G_WH_V[1]	Wh	Real energy L2, consumed
19066	float	RD/WR	_G_WH_V[2]	Wh	Real energy L3, consumed
19068	float	RD/WR	_G_V_HT_SUML13	Wh	Real energy L1..L3, consumed

Address	Format	RD/WR	Designation	Unit	Note
19070	float	RD/WR	_G_WH_Z[0]	Wh	Real energy L1, delivered
19072	float	RD/WR	_G_WH_Z[1]	Wh	Real energy L2, delivered
19074	float	RD/WR	_G_WH_Z[2]	Wh	Real energy L3, delivered
19076	float	RD/WR	_G_WH_Z_SUML3	Wh	Real energy L1...L3, delivered
19078	float	RD/WR	_G_WH_S[0]	VAh	Apparent energy L1
19080	float	RD/WR	_G_WH_S[1]	VAh	Apparent energy L2
19082	float	RD/WR	_G_WH_S[2]	VAh	Apparent energy L3
19084	float	RD/WR	_G_WH_S_SUML13	VAh	Apparent energy L1...L3
19086	float	RD	_G_QH[0]	varh	Reactive energy L1 (fundamental comp.)
19088	float	RD	_G_QH[1]	varh	Reactive energy L2 (fundamental comp.)
19090	float	RD	_G_QH[2]	varh	Reactive energy L3 (fundamental comp.)
19092	float	RD	_G_QH_SUML13	varh	Reactive energy Lsum3=L1...L3 (fundamental comp.)
19094	float	RD/WR	_G_IQH[0]	varh	Reactive energy, inductive L1 (fundamental comp.)
19096	float	RD/WR	_G_IQH[1]	varh	Reactive energy, inductive L2 (fundamental comp.)
19098	float	RD/WR	_G_IQH[2]	varh	Reactive energy, inductive L3 (fundamental comp.)
19100	float	RD/WR	_G_IQH_SUML13	varh	Reactive energy, inductive L1..L3 (fundamental comp.)
19102	float	RD/WR	_G_CQH[0]	varh	Reactive energy, capacitive L1 (fundamental comp.)
19104	float	RD/WR	_G_CQH[1]	varh	Reactive energy, capacitive L2 (fundamental comp.)
19106	float	RD/WR	_G_CQH[2]	varh	Reactive energy, capacitive L3 (fundamental comp.)
19108	float	RD/WR	_G_CQH_SUML13	varh	Reactive energy, capacitive sum=L1..L3 (fundamental comp.)
19110	float	RD	_G_THD_ULN[0]	%	Harmonic, THD,U L1-N
19112	float	RD	_G_THD_ULN[1]	%	Harmonic, THD,U L2-N
19114	float	RD	_G_THD_ULN[2]	%	Harmonic, THD,U L3-N
19116	float	RD	_G_THD_ILN[0]	%	Harmonic, THD,I L1
19118	float	RD	_G_THD_ILN[1]	%	Harmonic, THD,I L2
19120	float	RD	_G_THD_ILN[2]	%	Harmonic, THD,I L3
19122	float	RD	_G_I5	A	UMG806 I5 inputs
19124	float	RD	_G_PT100	°C	UMG806 PT100
19126	float	RD	_EI1_I[0]	mA	EI1 Module current inputs
19128	float	RD	_EI1_I[1]	mA	EI1 Module current inputs
19130	float	RD	_EI1_I[2]	mA	EI1 Module current inputs
19132	float	RD	_EI1_I[3]	mA	EI1 Module current inputs

Address	Format	RD/WR	Designation	Unit	Note
19134	float	RD	RCM_percent	%	RCM Value percent
19136	float	RD	RCM_limit_percent	%	RCM Limit percent
19138	float	RD	RCM_Status		Bit 0: ok Bit 1: RCM alarm Bit 2: RCM warning Bit 3: RCM alarm and RCM warning Bit 4: RCM over current Bit 5: RCM alarm and RCM over current Bit 6: RCM warning and RCM over current Bit 7: RCM alarm, RCM warning and RCM over current
19139-19164	-	-	-		Reserve
19165	short	RD	_DO_OUTPUT		Bit 0: EI1_DO1 Bit 1: EI1_DO2 Bit 2: ED1_DO1 Bit 3: ED1_DO2 0: OFF; 1:ON
19166	short	RD	_DI_INPUT		Bit 0: ED1_DI1 Bit 1: ED1_DI2 Bit 2: ED1_DI3 Bit 3: ED1_DI4 0: OFF; 1:ON
19167	int	RD	PT100 Status		0: OK; 1: Error
19168	long	RD	Alarm Status		0: OFF, 1:ON Bit 0: Un high alarm Bit 1: Un low alarm Bit 2: UI high alarm Bit 3: UI low alarm Bit 4: I1, I2, I3 high alarm Bit 5: I1, I2, I3 low alarm Bit 6: F high alarm Bit 7: F low alarm Bit 8: Psum high alarm Bit 9: Psum low alarm Bit 10: Qsum high alarm Bit 11: Qsum low alarm Bit 12: Ssum high alarm Bit 13: Ssum low alarm Bit 14: PFsum low alarm Bit 15: THDU high alarm Bit 16: THDI high alarm Bit 17: Uunb high alarm Bit 18: Iunb high alarm Bit 19: Pt100 high alarm Bit 20: Pt100 low alarm

Average values

Adress	Format	RD/WR	Designation	Unit	Note
19240	float	RD	_AVG_ULN[0]	V	Voltage L1
19242	float	RD	_AVG_ULN[1]	V	Voltage L2
19244	float	RD	_AVG_ULN[2]	V	Voltage L3
19246	float	RD	_AVG_ILN[0]	A	Current L1-N
19248	float	RD	_AVG_ILN[1]	A	Current L2-N
19250	float	RD	_AVG_ILN[2]	A	Current L3-N
19252	float	RD	_AVG_ILN[3]	A	Current Sum L1-L3
19254	float	RD	_AVG_PLN[0]	W	Real power L1-N
19256	float	RD	_AVG_PLN[1]	W	Real power L2-N
19258	float	RD	_AVG_PLN[2]	W	Real power L3-N
19260	float	RD	_AVG_P_SUM	W	Psum3=P1+P2+P3
19262	float	RD	_AVG_SLN[0]	VA	Apparent power L1-N
19264	float	RD	_AVG_SLN[1]	VA	Apparent power L2-N
19266	float	RD	_AVG_SLN[2]	VA	Apparent power L3-N
19268	float	RD	_AVG_S_SUM	VA	Sum; Ssum=S1+S2+S3
19270	float	RD	_AVG_COS_PH[0]	-	CosPhi; UL1 IL1 (fundamental comp.)
19272	float	RD	_AVG_COS_PH[1]	-	CosPhi; UL1 IL2 (fundamental comp.)
19274	float	RD	_AVG_COS_PH[2]	-	CosPhi; UL1 IL3 (fundamental comp.)
19276	float	RD	_AVG_COS_SUM		
19278	float	RD	_AVG_QLN[0]	var	Reactive power L1 (fundamental comp.)
19280	float	RD	_AVG_QLN[1]	var	Reactive power L2 (fundamental comp.)
19282	float	RD	_AVG_QLN[2]	var	Reactive power L3 (fundamental comp.)
19284	float	RD	_AVG_Q_SUM3	var	Qsum3=Q1+Q2+Q3
19286	float	RD	_AVG_EI1[0]	mA	EI1 Module current inputs
19288	float	RD	_AVG_EI1[1]	mA	EI1 Module current inputs
19290	float	RD	_AVG_EI1[2]	mA	EI1 Module current inputs
19292	float	RD	_AVG_EI1[3]	mA	EI1 Module current inputs
19293	float	RD	_AVG_PT100	°C	UMG 806 PT100

Data and time

Address	Format	RD/WR	Designation	Unit	Note
0	long64	RD			
4	int	RD			
6	short	RD/WR	_DAY		Day (1..31)
7	short	RD/WR	_MONTH		Month (0=Jan,..11=Dec)
8	short	RD/WR	_YEAR		Year
9	short	RD/WR	_HOUR	h	Hour (0..24)
10	short	RD/WR	_MIN	min	Minute (0..59)
11	short	RD/WR	_SEC	s	Second (0..59)
12	short	RD	_WEEKDAY		Weekday (0=Sun,..6=Sat)

Fourier analysis

Address	Format	RD/WR	Designation	Unit	Note
13	float	RD	_FFT_UL1[0]	%	U L1 Harmonic 2.
15	float	RD	_FFT_UL1[1]	%	U L1 Harmonic 3.
17	float	RD	_FFT_UL1[2]	%	U L1 Harmonic 4.
19	float	RD	_FFT_UL1[3]	%	U L1 Harmonic 5.
21	float	RD	_FFT_UL1[4]	%	U L1 Harmonic 6.
23	float	RD	_FFT_UL1[5]	%	U L1 Harmonic 7.
25	float	RD	_FFT_UL1[6]	%	U L1 Harmonic 8.
27	float	RD	_FFT_UL1[7]	%	U L1 Harmonic 9.
29	float	RD	_FFT_UL1[8]	%	U L1 Harmonic 10.
31	float	RD	_FFT_UL1[9]	%	U L1 Harmonic 11.
33	float	RD	_FFT_UL1[10]	%	U L1 Harmonic 12.
35	float	RD	_FFT_UL1[11]	%	U L1 Harmonic 13.
37	float	RD	_FFT_UL1[12]	%	U L1 Harmonic 14.
39	float	RD	_FFT_UL1[13]	%	U L1 Harmonic 15.
41	float	RD	_FFT_UL1[14]	%	U L1 Harmonic 16.
43	float	RD	_FFT_UL1[15]	%	U L1 Harmonic 17.
45	float	RD	_FFT_UL1[16]	%	U L1 Harmonic 18.
47	float	RD	_FFT_UL1[17]	%	U L1 Harmonic 19.
49	float	RD	_FFT_UL1[18]	%	U L1 Harmonic 20.
51	float	RD	_FFT_UL1[19]	%	U L1 Harmonic 21.
53	float	RD	_FFT_UL1[20]	%	U L1 Harmonic 22.
55	float	RD	_FFT_UL1[21]	%	U L1 Harmonic 23.
57	float	RD	_FFT_UL1[22]	%	U L1 Harmonic 24.
59	float	RD	_FFT_UL1[23]	%	U L1 Harmonic 25.
61	float	RD	_FFT_UL1[24]	%	U L1 Harmonic 26.
63	float	RD	_FFT_UL1[25]	%	U L1 Harmonic 27.
65	float	RD	_FFT_UL1[26]	%	U L1 Harmonic 28.
67	float	RD	_FFT_UL1[27]	%	U L1 Harmonic 29.
69	float	RD	_FFT_UL1[28]	%	U L1 Harmonic 30.
71	float	RD	_FFT_UL1[29]	%	U L1 Harmonic 31.
73	float	RD	_FFT_UL2[0]	%	U L2 Harmonic 2.

Address	Format	RD/WR	Designation	Unit	Note
75	float	RD	_FFT_UL2[1]	%	U L2 Harmonic 3.
77	float	RD	_FFT_UL2[2]	%	U L2 Harmonic 4.
79	float	RD	_FFT_UL2[3]	%	U L2 Harmonic 5.
81	float	RD	_FFT_UL2[4]	%	U L2 Harmonic 6.
83	float	RD	_FFT_UL2[5]	%	U L2 Harmonic 7.
85	float	RD	_FFT_UL2[6]	%	U L2 Harmonic 8.
87	float	RD	_FFT_UL2[7]	%	U L2 Harmonic 9.
89	float	RD	_FFT_UL2[8]	%	U L2 Harmonic 10.
91	float	RD	_FFT_UL2[9]	%	U L2 Harmonic 11.
93	float	RD	_FFT_UL2[10]	%	U L2 Harmonic 12.
95	float	RD	_FFT_UL2[11]	%	U L2 Harmonic 13.
97	float	RD	_FFT_UL2[12]	%	U L2 Harmonic 14.
99	float	RD	_FFT_UL2[13]	%	U L2 Harmonic 15.
101	float	RD	_FFT_UL2[14]	%	U L2 Harmonic 16.
103	float	RD	_FFT_UL2[15]	%	U L2 Harmonic 17.
105	float	RD	_FFT_UL2[16]	%	U L2 Harmonic 18.
107	float	RD	_FFT_UL2[17]	%	U L2 Harmonic 19.
109	float	RD	_FFT_UL2[18]	%	U L2 Harmonic 20.
111	float	RD	_FFT_UL2[19]	%	U L2 Harmonic 21.
113	float	RD	_FFT_UL2[20]	%	U L2 Harmonic 22.
115	float	RD	_FFT_UL2[21]	%	U L2 Harmonic 23.
117	float	RD	_FFT_UL2[22]	%	U L2 Harmonic 24.
119	float	RD	_FFT_UL2[23]	%	U L2 Harmonic 25.
121	float	RD	_FFT_UL2[24]	%	U L2 Harmonic 26.
123	float	RD	_FFT_UL2[25]	%	U L2 Harmonic 27.
125	float	RD	_FFT_UL2[26]	%	U L2 Harmonic 28.
127	float	RD	_FFT_UL2[27]	%	U L2 Harmonic 29.
129	float	RD	_FFT_UL2[28]	%	U L2 Harmonic 30.
131	float	RD	_FFT_UL2[29]	%	U L2 Harmonic 31.
133	float	RD	_FFT_UL3[0]	%	U L3 Harmonic 2.
135	float	RD	_FFT_UL3[1]	%	U L3 Harmonic 3.
137	float	RD	_FFT_UL3[2]	%	U L3 Harmonic 4.
139	float	RD	_FFT_UL3[3]	%	U L3 Harmonic 5.
141	float	RD	_FFT_UL3[4]	%	U L3 Harmonic 6.
143	float	RD	_FFT_UL3[5]	%	U L3 Harmonic 7.
145	float	RD	_FFT_UL3[6]	%	U L3 Harmonic 8.
147	float	RD	_FFT_UL3[7]	%	U L3 Harmonic 9.
149	float	RD	_FFT_UL3[8]	%	U L3 Harmonic 10.
151	float	RD	_FFT_UL3[9]	%	U L3 Harmonic 11.
153	float	RD	_FFT_UL3[10]	%	U L3 Harmonic 12.
155	float	RD	_FFT_UL3[11]	%	U L3 Harmonic 13.
157	float	RD	_FFT_UL3[12]	%	U L3 Harmonic 14.
159	float	RD	_FFT_UL3[13]	%	U L3 Harmonic 15.
161	float	RD	_FFT_UL3[14]	%	U L3 Harmonic 16.
163	float	RD	_FFT_UL3[15]	%	U L3 Harmonic 17.

Address	Format	RD/WR	Designation	Unit	Note
165	float	RD	_FFT_UL3[16]	%	U L3 Harmonic 18.
167	float	RD	_FFT_UL3[17]	%	U L3 Harmonic 19.
169	float	RD	_FFT_UL3[18]	%	U L3 Harmonic 20.
171	float	RD	_FFT_UL3[19]	%	U L3 Harmonic 21.
173	float	RD	_FFT_UL3[20]	%	U L3 Harmonic 22.
175	float	RD	_FFT_UL3[21]	%	U L3 Harmonic 23.
177	float	RD	_FFT_UL3[22]	%	U L3 Harmonic 24.
179	float	RD	_FFT_UL3[23]	%	U L3 Harmonic 25.
181	float	RD	_FFT_UL3[24]	%	U L3 Harmonic 26.
183	float	RD	_FFT_UL3[25]	%	U L3 Harmonic 27.
185	float	RD	_FFT_UL3[26]	%	U L3 Harmonic 28.
187	float	RD	_FFT_UL3[27]	%	U L3 Harmonic 29.
189	float	RD	_FFT_UL3[28]	%	U L3 Harmonic 30.
191	float	RD	_FFT_UL3[29]	%	U L3 Harmonic 31.
193	float	RD	_FFT_IL1[0]	%	I L1 Harmonic 2.
195	float	RD	_FFT_IL1[1]	%	I L1 Harmonic 3.
197	float	RD	_FFT_IL1[2]	%	I L1 Harmonic 4.
199	float	RD	_FFT_IL1[3]	%	I L1 Harmonic 5.
201	float	RD	_FFT_IL1[4]	%	I L1 Harmonic 6.
203	float	RD	_FFT_IL1[5]	%	I L1 Harmonic 7.
205	float	RD	_FFT_IL1[6]	%	I L1 Harmonic 8.
207	float	RD	_FFT_IL1[7]	%	I L1 Harmonic 9.
209	float	RD	_FFT_IL1[8]	%	I L1 Harmonic 10.
211	float	RD	_FFT_IL1[9]	%	I L1 Harmonic 11.
213	float	RD	_FFT_IL1[10]	%	I L1 Harmonic 12.
215	float	RD	_FFT_IL1[11]	%	I L1 Harmonic 13.
217	float	RD	_FFT_IL1[12]	%	I L1 Harmonic 14.
219	float	RD	_FFT_IL1[13]	%	I L1 Harmonic 15.
221	float	RD	_FFT_IL1[14]	%	I L1 Harmonic 16.
223	float	RD	_FFT_IL1[15]	%	I L1 Harmonic 17.
225	float	RD	_FFT_IL1[16]	%	I L1 Harmonic 18.
227	float	RD	_FFT_IL1[17]	%	I L1 Harmonic 19.
229	float	RD	_FFT_IL1[18]	%	I L1 Harmonic 20.
231	float	RD	_FFT_IL1[19]	%	I L1 Harmonic 21.
233	float	RD	_FFT_IL1[20]	%	I L1 Harmonic 22.
235	float	RD	_FFT_IL1[21]	%	I L1 Harmonic 23.
237	float	RD	_FFT_IL1[22]	%	I L1 Harmonic 24.
239	float	RD	_FFT_IL1[23]	%	I L1 Harmonic 25.
241	float	RD	_FFT_IL1[24]	%	I L1 Harmonic 26.
243	float	RD	_FFT_IL1[25]	%	I L1 Harmonic 27.
245	float	RD	_FFT_IL1[26]	%	I L1 Harmonic 28.
247	float	RD	_FFT_IL1[27]	%	I L1 Harmonic 29.
249	float	RD	_FFT_IL1[28]	%	I L1 Harmonic 30.
251	float	RD	_FFT_IL1[29]	%	I L1 Harmonic 31.
253	float	RD	_FFT_IL2[0]	%	I L2 Harmonic 2.
255	float	RD	_FFT_IL2[1]	%	I L2 Harmonic 3.

Address	Format	RD/WR	Designation	Unit	Note
257	float	RD	_FFT_IL2[2]	%	I L2 Harmonic 4.
259	float	RD	_FFT_IL2[3]	%	I L2 Harmonic 5.
261	float	RD	_FFT_IL2[4]	%	I L2 Harmonic 6.
263	float	RD	_FFT_IL2[5]	%	I L2 Harmonic 7.
265	float	RD	_FFT_IL2[6]	%	I L2 Harmonic 8.
267	float	RD	_FFT_IL2[7]	%	I L2 Harmonic 9.
269	float	RD	_FFT_IL2[8]	%	I L2 Harmonic 10.
271	float	RD	_FFT_IL2[9]	%	I L2 Harmonic 11.
273	float	RD	_FFT_IL2[10]	%	I L2 Harmonic 12.
275	float	RD	_FFT_IL2[11]	%	I L2 Harmonic 13.
277	float	RD	_FFT_IL2[12]	%	I L2 Harmonic 14.
279	float	RD	_FFT_IL2[13]	%	I L2 Harmonic 15.
281	float	RD	_FFT_IL2[14]	%	I L2 Harmonic 16.
283	float	RD	_FFT_IL2[15]	%	I L2 Harmonic 17.
285	float	RD	_FFT_IL2[16]	%	I L2 Harmonic 18.
287	float	RD	_FFT_IL2[17]	%	I L2 Harmonic 19.
289	float	RD	_FFT_IL2[18]	%	I L2 Harmonic 20.
291	float	RD	_FFT_IL2[19]	%	I L2 Harmonic 21.
293	float	RD	_FFT_IL2[20]	%	I L2 Harmonic 22.
295	float	RD	_FFT_IL2[21]	%	I L2 Harmonic 23.
297	float	RD	_FFT_IL2[22]	%	I L2 Harmonic 24.
299	float	RD	_FFT_IL2[23]	%	I L2 Harmonic 25.
301	float	RD	_FFT_IL2[24]	%	I L2 Harmonic 26.
303	float	RD	_FFT_IL2[25]	%	I L2 Harmonic 27.
305	float	RD	_FFT_IL2[26]	%	I L2 Harmonic 28.
307	float	RD	_FFT_IL2[27]	%	I L2 Harmonic 29.
309	float	RD	_FFT_IL2[28]	%	I L2 Harmonic 30.
311	float	RD	_FFT_IL2[29]	%	I L2 Harmonic 31.
313	float	RD	_FFT_IL3[0]	%	I L3 Harmonic 2.
315	float	RD	_FFT_IL3[1]	%	I L3 Harmonic 3.
317	float	RD	_FFT_IL3[2]	%	I L3 Harmonic 4.
319	float	RD	_FFT_IL3[3]	%	I L3 Harmonic 5.
321	float	RD	_FFT_IL3[4]	%	I L3 Harmonic 6.
323	float	RD	_FFT_IL3[5]	%	I L3 Harmonic 7.
325	float	RD	_FFT_IL3[6]	%	I L3 Harmonic 8.
327	float	RD	_FFT_IL3[7]	%	I L3 Harmonic 9.
329	float	RD	_FFT_IL3[8]	%	I L3 Harmonic 10.
331	float	RD	_FFT_IL3[9]	%	I L3 Harmonic 11.
333	float	RD	_FFT_IL3[10]	%	I L3 Harmonic 12.
335	float	RD	_FFT_IL3[11]	%	I L3 Harmonic 13.
337	float	RD	_FFT_IL3[12]	%	I L3 Harmonic 14.
339	float	RD	_FFT_IL3[13]	%	I L3 Harmonic 15.
341	float	RD	_FFT_IL3[14]	%	I L3 Harmonic 16.
343	float	RD	_FFT_IL3[15]	%	I L3 Harmonic 17.
345	float	RD	_FFT_IL3[16]	%	I L3 Harmonic 18.
347	float	RD	_FFT_IL3[17]	%	I L3 Harmonic 19.

Address	Format	RD/WR	Designation	Unit	Note
349	float	RD	_FFT_IL3[18]	%	I L3 Harmonic 20.
351	float	RD	_FFT_IL3[19]	%	I L3 Harmonic 21.
353	float	RD	_FFT_IL3[20]	%	I L3 Harmonic 22.
355	float	RD	_FFT_IL3[21]	%	I L3 Harmonic 23.
357	float	RD	_FFT_IL3[22]	%	I L3 Harmonic 24.
359	float	RD	_FFT_IL3[23]	%	I L3 Harmonic 25.
361	float	RD	_FFT_IL3[24]	%	I L3 Harmonic 26.
363	float	RD	_FFT_IL3[25]	%	I L3 Harmonic 27.
365	float	RD	_FFT_IL3[26]	%	I L3 Harmonic 28.
367	float	RD	_FFT_IL3[27]	%	I L3 Harmonic 29.
369	float	RD	_FFT_IL3[28]	%	I L3 Harmonic 30.
371	float	RD	_FFT_IL3[29]	%	I L3 Harmonic 31.

Meter information

Address	Format	RD/WR	Designation	Note
911	uint32	RD	Meter Serial Num	
913	short	RD	Firmware Version	
914-915	-	-	Reserve	
916	char	RD	High Byte: EI1 Module 0: Disable 1: Enable Low Byte: ED1 Module 0: Disable 1: Enable	
917	char	RD	High Byte: EC1Module 0: Disable 1: Enable	
918	short	RD	UMG 806 Hardware version	
919	short	RD	EI1 Hardware version	
920	short	RD	ED1 Hardware version	
921	short	RD	EC1 Hardware version	

Measured value (250 ms measuring window)

Address	Format	RD/WR	Designation	Unit	Note
2029	float	RD	_THD_ULN[0]	%	Harmonic, THD, UL1-N
2031	float	RD	_THD_ULN[1]	%	Harmonic, THD, UL2-N
2033	float	RD	_THD_ULN[2]	%	Harmonic, THD, UL3-N
2035	float	RD			
2037	float	RD	_THD_IL[0]	%	Harmonic, THD,IL1-N
2039	float	RD	_THD_IL[1]	%	Harmonic, THD,IL2-N
2041	float	RD	_THD_IL[2]	%	Harmonic, THD,IL3-N
2043	float	RD			
2045	float	RD	_KFACT[0]		K-Factor, L
2047	float	RD	_KFACT[1]		K-Factor, L
2049	float	RD	_KFACT[2]		K-Factor, L
2051	float	RD			

Address	Format	RD/WR	Designation	Unit	Note
2053	float	RD	_ULN[0]	V	Voltage L1_N
2055	float	RD	_ULN[1]	V	Voltage L2_N
2057	float	RD	_ULN[2]	V	Voltage L3_N
2059	float	RD			
2061	float	RD	_ILN[0]	A	Apparent current,L1
2063	float	RD	_ILN[1]	A	Apparent current,L2
2065	float	RD	_ILN[2]	A	Apparent current,L3
2067	float	RD	_ILN[3]	A	Apparent current,L4
2069	float	RD	_PLN[0]	W	Real Power L1
2071	float	RD	_PLN[1]	W	Real Power L2
2073	float	RD	_PLN[2]	W	Real Power L3
2075	float	RD			
2077	float	RD	_QLN[0]	var	Reactive Power L1
2079	float	RD	_QLN[1]	var	Reactive Power L2
2081	float	RD	_QLN[2]	var	Reactive Power L3
2083	float	RD			
2085	float	RD	_SLN[0]	VA	Apparent power,L1
2087	float	RD	_SLN[1]	VA	Apparent power,L2
2089	float	RD	_SLN[2]	VA	Apparent power,L3
2091	float	RD			
2093	float	RD	_ULL[0]	V	Phase conductor, UL1-L2
2095	float	RD	_ULL[1]	V	Phase conductor, UL2-L3
2097	float	RD	_ULL[2]	V	Phase conductor, UL3-L1
2099	float	RD	_I_SUM3	A	Vector sum, IN=I1+I2+I3
2101	float	RD			
2103	float	RD	_S_SUM3	VA	Sum,S=S1+S2+S3
2105	float	RD	_P_SUM3	W	Sum,P=P1+P2+P3
2107	float	RD	_Q_SUM3	var	Mains frequency reactive power,sum,Q=Q1+Q2+Q3
2109-2142					Reserve
2143	float	RD	PF[0]		Power factor, L1
2145	float	RD	PF[1]		Power factor, L2
2147	float	RD	PF[2]		Power factor, L3
2149	float	RD	PF_SUM		Power factor, sum
2151	float	RD	_PHASE[0]	°	Phase, UL1 IL1
2153	float	RD	_PHASE[1]	°	Phase, UL2 IL2
2155	float	RD	_PHASE[2]	°	Phase, UL3 IL3
2157	float	RD			
2159	float	RD	_COS_PHI[0]		Fund.power factor, CosPhi; UL1 IL1
2161	float	RD	_COS_PHI[1]		Fund.power factor, CosPhi; UL2 IL2
2163	float	RD	_COS_PHI[2]		Fund.power factor, CosPhi; UL3 IL3
2165	float	RD	_COS_PHI_SUM		Total fund.power factory
2167	float	RD	_IND_CAP[0]		Sign,QL1, +1=ind.,-1=cap.
2169	float	RD	_IND_CAP[1]		Sign,QL1, +1=ind.,-1=cap.
2171	float	RD	_IND_CAP[2]		Sign,QL1, +1=ind.,-1=cap.

Address	Format	RD/WR	Designation	Unit	Note
2173	float	RD			
2175	float	RD	_FREQ	Hz	Measured frequency
2177	float	RD	_UN	V	Zero sequence,voltage
2179	float	RD	_UM	V	Positive sequence,voltage
2181	float	RD	_UG	V	Negative sequence,voltage
2183	float	RD	_U_SYM	%	Unsymmetrical;voltage

Address	Format	RD/WR	Designation	Unit	Note
2185	float	RD			
2187	float	RD	_IN	A	Zero sequence,current
2189	float	RD	_IM	A	Positive sequence,current
2191	float	RD	_IG	A	Negative sequence,current
2193	float	RD	_I_SYM	%	Unsymmetrical;current

Demand values

Address	Format	RD/WR	Designation	Unit	Note
4215	float	RD	_PreDemand[0]	The unit is related to the setting data type, and the data unit in Measure Values has been	Demand data of last cycle
4217	float	RD	_PreDemand[1]		
4219	float	RD	_PreDemand[2]		
4221	float	RD	_PreDemand[3]		
4223	float	RD	_PreDemand[4]		
4225	float	RD	_PreDemand[5]		
4227	float	RD	_PreDemand[6]		
4229	float	RD	_PreDemand[7]		
4231	float	RD	_PreDemand[8]		
4233	float	RD	_NowDemand[0]	The unit is related to the setting data type, and the data unit in Measure Values has been	Demand data of present cycle
4235	float	RD	_NowDemand[1]		
4237	float	RD	_NowDemand[2]		
4239	float	RD	_NowDemand[3]		
4241	float	RD	_NowDemand[4]		
4243	float	RD	_NowDemand[5]		
4245	float	RD	_NowDemand[6]		
4247	float	RD	_NowDemand[7]		
4249	float	RD	_NowDemand[8]		
4251	float	RD	_MaxDemand[0]	The unit is related to the setting data type, and the data unit in Measure Values has been	Max. demand of history
4253	float	RD	_MaxDemand[1]		
4255	float	RD	_MaxDemand[2]		
4257	float	RD	_MaxDemand[3]		
4259	float	RD	_MaxDemand[4]		
4261	float	RD	_MaxDemand[5]		
4263	float	RD	_MaxDemand[6]		
4265	float	RD	_MaxDemand[7]		
4267	float	RD	_MaxDemand[8]		

Address	Format	RD/WR	Designation	Unit	Note
4269	char	RD	_Time_MaxDemand[0]	Demand Max Time	High Byte: Year Low Byte: Month
4270	char	RD			High Byte: Day Low Byte: Hour
4271	char	RD			High Byte: Minute Low Byte: Second
4272	char	RD	_Time_MaxDemand[1]	Demand Max Time	High Byte: Year Low Byte: Month
4273	char	RD			High Byte: Day Low Byte: Hour
4274	char	RD			High Byte: Minute Low Byte: Second
4275	char	RD	_Time_MaxDemand[2]	Demand Max Time	High Byte: Year Low Byte: Month
4276	char	RD			High Byte: Day Low Byte: Hour
4277	char	RD			High Byte: Minute Low Byte: Second
4278	char	RD	_Time_MaxDemand[3]	Demand Max Time	High Byte: Year Low Byte: Month
4279	char	RD			High Byte: Day Low Byte: Hour
4280	char	RD			High Byte: Minute Low Byte: Second
4281	char	RD	_Time_MaxDemand[4]	Demand Max Time	High Byte: Year Low Byte: Month
4282	char	RD			High Byte: Day Low Byte: Hour
4283	char	RD			High Byte: Minute Low Byte: Second
4284	char	RD	_Time_MaxDemand[5]	Demand Max Time	High Byte: Year Low Byte: Month
4285	char	RD			High Byte: Day Low Byte: Hour
4286	char	RD			High Byte: Minute Low Byte: Second
4287	char	RD	_Time_MaxDemand[6]	Demand Max Time	High Byte: Year Low Byte: Month
4288	char	RD			High Byte: Day Low Byte: Hour
4289	char	RD			High Byte: Minute Low Byte: Second

Address	Format	RD/WR	Designation	Unit	Note
4290	char	RD	_Time _MaxDemand[7]	Demand Max Time	High Byte: Year Low Byte: Month
4291	char	RD			High Byte: Day Low Byte: Hour
4292	char	RD			High Byte: Minute Low Byte: Second
4293	char	RD	_Time _MaxDemand[8]	Demand Max Time	High Byte: Year Low Byte: Month
4294	char	RD			High Byte: Day Low Byte: Hour
4295	char	RD			High Byte: Minute Low Byte: Second

Maximum Minimum values

Address	Format	RD/WR	Designation	Unit	Note
6397	float	RD	_ULN_MAX[0]	V	Maximum, UL1-N
6399	float	RD	_ULN_MAX[1]	V	Maximum, UL2-N
6401	float	RD	_ULN_MAX[2]	V	Maximum, UL3-N
6403	float	RD	_ULL_MAX[0]	V	Maximum, UL1-L2
6405	float	RD	_ULL_MAX[1]	V	Maximum, UL2-L3
6407	float	RD	_ULL_MAX[2]	V	Maximum, UL3-L1
6409	float	RD	_ILN_MAX[0]	A	Maximum, I L1
6411	float	RD	_ILN_MAX[1]	A	Maximum, I L2
6413	float	RD	_ILN_MAX[2]	A	Maximum, I L3
6415	float	RD	_ILN_MAX[3]	A	Maximum, I L4
6417	float	RD	_P_SUM3_MAX	W	Maximum, Psum3=P1+P2+P3
6419	float	RD	_Q_SUM3_MAX	var	Maximum, Qsum3=Q1+Q2+Q3
6421	float	RD	_S_SUM3_MAX	VA	Maximum, Ssum3=S1+S2+S3
6423	float	RD	_PF_SUM3_MAX		Maximum, Psum3/Ssum3
6425	float	RD	_FREQ_MAX	Hz	Maximum, requency
6427	float	RD	_PT100_MAX	°C	PT100
6429-6444	-	-	-	-	Reserve
6445	float	RD	_ULN_MIN[0]	V	Minimum, UL1-N
6447	float	RD	_ULN_MIN[1]	V	Minimum, UL2-N
6449	float	RD	_ULN_MIN[2]	V	Minimum, UL3-N
6451	float	RD	_ULL_MIN[0]	V	Minimum, UL1-L2
6453	float	RD	_ULL_MIN[1]	V	Minimum, UL2-L3
6455	float	RD	_ULL_MIN[2]	V	Minimum, UL3-L1
6457	float	RD	_ILN_MIN[0]	A	Minimum, I L1
6459	float	RD	_ILN_MIN[1]	A	Minimum, I L2
6461	float	RD	_ILN_MIN[2]	A	Minimum, I L3
6463	float	RD	_ILN_MIN[3]	A	Minimum, I L4
6465	float	RD	_P_SUM3_MIN		Minimum, Psum3=P1+P2+P3
6467	float	RD	_Q_SUM3_MIN	var	Minimum, Qsum3=Q1+Q2+Q3
6469	float	RD	_S_SUM3_MIN	VA	Minimum, Ssum3=S1+S2+S3
6471	float	RD	_PF_SUM3_MIN		Minimum, Psum3/Ssum3

Address	Format	RD/WR	Designation	Unit	Note
6473	float	RD	_FREQ_MIN	Hz	Minimum, frequency
6475	float	RD	_PT100_MIN	°C	PT100
6477-6492	-	-	-	-	Reserve
6493	char	RD	_Time_ULN_MAX[0]		High Byte: Year Low Byte: Month
6494	char	RD			High Byte: Day Low Byte: Hour
6495	char	RD			High Byte: Minute Low Byte: Second
6496	char	RD	_Time_ULN_MAX[1]		High Byte: Year Low Byte: Month
6497	char	RD			High Byte: Day Low Byte: Hour
6498	char	RD			High Byte: Minute Low Byte: Second
6499	char	RD	_Time_ULN_MAX[2]		High Byte: Year Low Byte: Month
6500	char	RD			High Byte: Day Low Byte: Hour
6501	char	RD			High Byte: Minute Low Byte: Second
6502	char	RD	_Time_ULL_MAX[0]		High Byte: Year Low Byte: Month
6503	char	RD			High Byte: Day Low Byte: Hour
6504	char	RD			High Byte: Minute Low Byte: Second
6505	char	RD	_Time_ULL_MAX[1]		High Byte: Year Low Byte: Month
6506	char	RD			High Byte: Day Low Byte: Hour
6507	char	RD			High Byte: Minute Low Byte: Second
6508	char	RD	_Time_ULL_MAX[2]		High Byte: Year Low Byte: Month
6509	char	RD			High Byte: Day Low Byte: Hour
6510	char	RD			High Byte: Minute Low Byte: Second
6511	char	RD	_Time_ILN_MAX[0]		High Byte: Year Low Byte: Month
6512	char	RD			High Byte: Day Low Byte: Hour
6513	char	RD			High Byte: Minute Low Byte: Second

Address	Format	RD/WR	Designation	Unit	Note
6514	char	RD	_Time_ILN_MAX[1]		High Byte: Year Low Byte: Month
6515	char	RD			High Byte: Day Low Byte: Hour
6516	char	RD			High Byte: Minute Low Byte: Second
6517	char	RD	_Time_ILN_MAX[2]		High Byte: Year Low Byte: Month
6518	char	RD			High Byte: Day Low Byte: Hour
6519	char	RD			High Byte: Minute Low Byte: Second
6520	char	RD	_Time_ILN_MAX[3]		High Byte: Year Low Byte: Month
6521	char	RD			High Byte: Day Low Byte: Hour
6522	char	RD			High Byte: Minute Low Byte: Second
6523	char	RD	_Time_P_SUM3_MAX		High Byte: Year Low Byte: Month
6524	char	RD			High Byte: Day Low Byte: Hour
6525	char	RD			High Byte: Minute Low Byte: Second
6526	char	RD	_Time_Q_SUM3_MAX		High Byte: Year Low Byte: Month
6527	char	RD			High Byte: Day Low Byte: Hour
6528	char	RD			High Byte: Minute Low Byte: Second
6529	char	RD	_Time_S_SUM3_MAX		High Byte: Year Low Byte: Month
6530	char	RD			High Byte: Day Low Byte: Hour
6531	char	RD			High Byte: Minute Low Byte: Second
6532	char	RD	_Time_PF_SUM_MAX		High Byte: Year Low Byte: Month
6533	char	RD			High Byte: Day Low Byte: Hour
6534	char	RD			High Byte: Minute Low Byte: Second
6535	char	RD	_Time_FREQ_MAX		High Byte: Year Low Byte: Month
6536	char	RD			High Byte: Day Low Byte: Hour
6537	char	RD			High Byte: Minute Low Byte: Second

Address	Format	RD/WR	Designation	Unit	Note
6538	char	RD	_Time_PT100_MAX		High Byte: Year Low Byte: Month
6539	char	RD			High Byte: Day Low Byte: Hour
6540	char	RD			High Byte: Minute Low Byte: Second
6541-6564					Reserve
6565	char	RD	_Time_ULN_MIN[0]		High Byte: Year Low Byte: Month
6566	char	RD			High Byte: Day Low Byte: Hour
6567	char	RD			High Byte: Minute Low Byte: Second
6568	char	RD	_Time_ULN_MIN[1]		High Byte: Year Low Byte: Month
6569	char	RD			High Byte: Day Low Byte: Hour
6570	char	RD			High Byte: Minute Low Byte: Second
6571	char	RD	_Time_ULN_MIN[2]		High Byte: Year Low Byte: Month
6572	char	RD			High Byte: Day Low Byte: Hour
6573	char	RD			High Byte: Minute Low Byte: Second
6574	char	RD	_Time_ULL_MIN[0]		High Byte: Year Low Byte: Month
6575	char	RD			High Byte: Day Low Byte: Hour
6576	char	RD			High Byte: Minute Low Byte: Second
6577	char	RD	_Time_ULL_MIN[1]		High Byte: Year Low Byte: Month
6578	char	RD			High Byte: Day Low Byte: Hour
6579	char	RD			High Byte: Minute Low Byte: Second
6580	char	RD	_Time_ULL_MIN[2]		High Byte: Year Low Byte: Month
6581	char	RD			High Byte: Day Low Byte: Hour
6582	char	RD			High Byte: Minute Low Byte: Second
6583	char	RD	_Time_ILN_MIN[0]		High Byte: Year Low Byte: Month
6584	char	RD			High Byte: Day Low Byte: Hour
6585	char	RD			High Byte: Minute Low Byte: Second

Address	Format	RD/WR	Designation	Unit	Note
6586	char	RD	_Time_ILN_MIN[1]		High Byte: Year Low Byte: Month
6587	char	RD			High Byte: Day Low Byte: Hour
6588	char	RD			High Byte: Minute Low Byte: Second
6589	char	RD	_Time_ILN_MIN[2]		High Byte: Year Low Byte: Month
6590	char	RD			High Byte: Day Low Byte: Hour
6591	char	RD			High Byte: Minute Low Byte: Second
6592	char	RD	_Time_ILN_MIN[3]		High Byte: Year Low Byte: Month
6593	char	RD			High Byte: Day Low Byte: Hour
6594	char	RD			High Byte: Minute Low Byte: Second
6595	char	RD	_Time_P_SUM3_MIN		High Byte: Year Low Byte: Month
6596	char	RD			High Byte: Day Low Byte: Hour
6597	char	RD			High Byte: Minute Low Byte: Second
6598	char	RD	_Time_Q_SUM3_MIN		High Byte: Year Low Byte: Month
6599	char	RD			High Byte: Day Low Byte: Hour
6660	char	RD			High Byte: Minute Low Byte: Second
6661	char	RD	_Time_S_SUM3_MIN		High Byte: Year Low Byte: Month
6662	char	RD			High Byte: Day Low Byte: Hour
6663	char	RD			High Byte: Minute Low Byte: Second
6664	char	RD	_Time_PF_SUM_MIN		High Byte: Year Low Byte: Month
6665	char	RD			High Byte: Day Low Byte: Hour
6666	char	RD			High Byte: Minute Low Byte: Second
6667	char	RD	_Time_FREQ_MIN		High Byte: Year Low Byte: Month
6668	char	RD			High Byte: Day Low Byte: Hour
6669	char	RD			High Byte: Minute Low Byte: Second

Address	Format	RD/WR	Designation	Unit	Note
6670	char		_Time_PT100_MIN		High Byte: Year Low Byte: Month
6671	char				High Byte: Day Low Byte: Hour
6672	char				High Byte: Minute Low Byte: Second
6673-6696	-	-	-	-	Reserve

System Set (for EC1 Homepage)

Address	Format	RD/WR	Designation1	Designation2
29996	short	RD/WR	I4 primary	1-9999
29997	short	RD/WR	I4 secondary	1-9999
29998	short	RD/WR	I5 primary	1-9999
29999	short	RD/WR	I5 secondary	1-9999
30000	short	RD/WR	Main body: Slave address	1-247
30001	short	RD/WR	Main body: Baud rate	0: 9600 bps 1: 19200 bps 2: 38400 bps 3: 57600 bps 4: 115200 bps
30002	short	RD/WR	Main body: Data format	0: N,8,1 1: E,8,1 2: O,8,1 3: N,8,2
30003				
30004	short	RD/WR	Wiring	0: 3P4W 1: 3P3W 2: 1P2W
30005	short	RD	Grid frequency	0: 50 Hz 1: 60 Hz
30006	short	RD	Current type1	0: 5A 1: Direct type
30007	short	RD	Current type2	0: 5A 1: Direct type
30008	Long	RD/WR	PT primary	1 ... 999999 V
30010	Long	RD/WR	CT primary	1 ... 999999 A
30012	short	RD/WR	PT secondary	1 ... 690 V
30013	short	RD/WR	CT secondary	1 ... 6 A
30014	short	RD/WR	RCM Limit value	0-65535 unit 0.01 mA
30015	short	RD/WR	Temperature Compensate	-999 ... 999 (0.1 °C)
30016	short	RD/WR	Demand1 item	0: Ua, 1: Ub, 2: Uc 3: Uab, 4: Ubc, 5: Uca 6: Ia, 7: Ib, 8: c, 9: In, 10: Pa, 11: Pb, 12: Pc, 13: P, 14: Qa, 15: Qb, 16: Qc, 17: Q, 18: Sa, 19: S2, 20: S3, 21: S, 22: PFa, 23: PFb, 24: PFc, 25: PF
30017	short	RD/WR	Demand2 item	The Same to Demand1 Item
30018	short	RD/WR	Demand3 item	The Same to Demand1 Item
30019	short	RD/WR	Demand4 item	The Same to Demand1 Item
30020	short	RD/WR	Demand5 item	The Same to Demand1 Item
30021	short	RD/WR	Demand6 item	The Same to Demand1 Item
30022	short	RD/WR	Demand7 item	The Same to Demand1 Item
30023	short	RD/WR	Demand8 item	The Same to Demand1 Item
30024	short	RD/WR	Demand9 item	The Same to Demand1 Item
30025	short	RD/WR	Mode of demand	0: sliding block mode 1: fixed block mode
30026	short	RD/WR	Sliding time(t)	1-9999 s

Address	Format	RD/WR	Designation1	Designation2
30027	short	RD/WR	Demand period(T)	(1-30)xt
30028	short	RD/WR	Relay output EI1-DO1 mode	0: OFF 1: alarm 2: remote control
30029	short	RD/WR	Relay output EI1-DO1 Pulse width	0.1~99.99s 0.0: no pulse
30030	short	RD/WR	Relay output EI1-DO1 Item	0: V1 > 1: V1 < 2: V2 > 3: V2 < 4: V3 > 5: V3 < 6: Vn > 7: Vn < 8: V12 > 9: V12 < 10: V23 > 11: V23 < 12: V31 > 13: V31 < 14: VI > 15: VI < 16: VInavg > 17: VInavg < 18: VIlavg > 19: VIlavg < 20: I1 > 21: I1 < 22: I2 > 23: I2 < 24: I3 > 25: I3 < 26: I > 27: I < 28: Iavg > 29: Iavg < 30: In > 31: In < 32: P > 33: P < 34: Q > 35: Q < 36: S > 37: S < 38: PF> 39: PF< 40: F > 41: F < 42: Uunb > 43: Uunb < 44: Iunb > 45: Iunb < 46: THDu > 47: THDu < 48: THDi > 49: THDi < 50: Alarm State 1 51: Alarm State 0 52: DIX_1 53: DIX_0 54: DI1_1 55: DI1_0 56: DI2_1 57: DI2_0 58: DI3_1 59: DI3_0 60: DI4_1 61: DI4_0I
30031-30032	-	-	--	Reserve
30033	short	RD/WR	Relay outputs EI1-DO1 Delay Time	0.1 ... 99.99 s
30034	short	RD/WR	Relay output EI1-DO2 mode	0: OFF 1: alarm 2: remote control
30035	short	RD/WR	Relay output EI1-DO2 Pulse width	
30036	short	RD/WR	Relay output EI1-DO2 Item	
30037-30038	-	-	--	Reserve
30039	short		Relay outputs EI1-DO2 Delay Time	0.1 ... 99.99 s

Address	Format	RD/WR	Designation1	Designation2
30040	short	RD/WR	Relay output ED1-DO1 mode	0: OFF 1: alarm 2: remote control
30041	short	RD/WR	Relay output ED1-DO1 Pulse width	
30042	short	RD/WR	Relay output ED1-DO1 Item	Same above
30043-30044	-	-	--	Reserve
30045	short	RD/WR	Relay outputs ED1-DO1 Delay Time	0.1 ... 99.99 s
30046	short	RD/WR	Relay output ED1-DO2 mode	0: OFF 1: alarm 2: remote control
30047	short	RD/WR	Relay output ED1-DO2 Pulse width	0.1 ... 99.99 s
30048	short	RD/WR	Relay output ED1-DO2 Item	Same above
30049-30050	-	-	--	Reserve
30051	short	RD/WR	Relay outputs ED1-DO2 Delay Time	0.1 ... 99.99 s
30052	float	RD/WR	EI1_DO1 Alarm Value	
30054	float	RD/WR	EI1_DO1 Alarm Hys	
30056	float	RD/WR	EI1_DO2 Alarm Value	
30058	float	RD/WR	EI1_DO2 Alarm Hys	
30060	float	RD/WR	ED1_DO1 Alarm Value	
30062	float	RD/WR	ED1_DO1 Alarm Hys	
30064	float	RD/WR	ED1_DO2 Alarm Value	
30066	float	RD/WR	ED1_DO2 Alarm Hys	
30068	float	RD/WR	Un Alarm High Value	V
30070	float	RD/WR	Un Alarm High Hys	V
30072	float	RD/WR	Un Alarm Low Value	V
30074	float	RD/WR	Un Alarm Low Hys	V
30076	float	RD/WR	Un Alarm Time Delay	s
30078	float	RD/WR	UI Alarm High Value	V
30080	float	RD/WR	UI Alarm High Hys	V
30082	float	RD/WR	UI Alarm Low Value	V
30084	float	RD/WR	UI Alarm Low Hys	V
30086	float	RD/WR	UI Alarm Time Delay	s
30088	float	RD/WR	In Alarm High Value	A
30090	float	RD/WR	In Alarm High Hys	A
30092	float	RD/WR	In Alarm Low Value	A
30094	float	RD/WR	In Alarm Low Hys	A
30096	float	RD/WR	In Alarm Time Delay	s
30098	float	RD/WR	F Alarm High Value	Hz
30100	float	RD/WR	F Alarm High Hys	Hz

Address	Format	RD/WR	Designation1	Designation2
30102	float	RD/WR	F Alarm Low Value	Hz
30104	float	RD/WR	F Alarm Low Hys	Hz
30106	float	RD/WR	In Alarm Time Delay	s
30108	float	RD/WR	Pall Alarm High Value	W
30110	float	RD/WR	Pall Alarm High Hys	W
30112	float	RD/WR	Pall Alarm Low Value	W
30114	float	RD/WR	Pall Alarm Low Hys	W
30116	float	RD/WR	Pall Alarm Time Delay	s
30118	float	RD/WR	PF Alarm High Value	
30120	float	RD/WR	PF Alarm High Hys	
30122	float	RD/WR	PF Alarm Time Dealy	s
30124	float	RD/WR	THDU Alarm High Value	%
30126	float	RD/WR	THDU Alarm High Hys	%
30128	float	RD/WR	THDU Alarm Time Delay	s
30130	float	RD/WR	THDI Alarm High Value	%
30132	float	RD/WR	THDI Alarm High Hys	%
30134	float	RD/WR	THDI Alarm Time Delay	s
30136	float	RD/WR	U_SYM Alarm High Value	
30138	float	RD/WR	U_SYM Alarm High Hys	
30140	float	RD/WR	U_SYM Alarm Time Delay	s
30142	float	RD/WR	I_SYM Alarm High Value	
30144	float	RD/WR	I_SYM Alarm High Hys	
30146	float	RD/WR	I_SYM Alarm Time Delay	s
30148	float	RD/WR	PT100 Alarm High Value	°C
30150	float	RD/WR	PT100 Alarm High Hys	
30152	float	RD/WR	PT100 Alarm Low Value	Hz
30154	float	RD/WR	PT100 Alarm Low Hys	Hz
30156	float	RD/WR	PT100 Alarm Delay	s
30158	short	RD/WR	Alarm Enable: 1: enable 0: closed Bit0: phase voltage high alarm enable Bit1: phase voltage low alarm enable Bit2: Line voltage high alarm enable Bit3: Line voltage low alarm enable Bit4: Current high alarm enable Bit5: Current low alarm enable Bit6: Frequency high alarm enable Bit7: Frequency low alarm enable Bit8: total active power high alarm enable Bit9: total active power low alarm enable Bit10: total reactive power high alarm enable Bit11: total reactive power low alarm enable Bit12: total apparent power high alarm enable Bit13: total apparent power low alarm enable Bit14: total power factor low alarm enable Bit15: total voltage harmonic distortion high alarm enable	
30159	short	RD/WR	Bit0:total current harmonic distortion high alarm enable; Bit1:current unbalance high alarm enable; Bit2:current unbalance low alarm enable; Bit3-Bit15: reserved	

Address	Format	RD/WR	Designation1	Designation2
30160	short	RD/WR	0: EI1-DO1 OFF 1: EI1-DO1 ON	EI1-DO1 mode is remote control, the register can write
30161	short	RD/WR	0: EI1-DO2 OFF 1: EI1-DO2 ON	EI1-DO2 mode is remote control, the register can write
30162	short	RD/WR	0: ED1-DO1 OFF 1: ED1-DO1 ON	ED1-DO1 mode is remote control, the register can write
30163	short	RD/WR	0: ED1-DO2 OFF 1: ED1-DO2 ON	ED1-DO2 mode is remote control, the register can write
30164-30172	-	-	--	Reserve
30173	short	RD/WR	Mode for limit: 0: not active, 1: static limit, 2: dynamic limit, 3: stepwise limit	
30174	ushor	RD/WR	Reference Value: 0: Active Power Sum L1-L3, 1: Apparent Power Sum L1-L3	
30175	short	RD/WR	Number of loads (for dynamic limit)	
30176	float	RD/WR	Residual current per load in Am- pere (for dynamic limit)	A
30178	float	RD/WR	Tolerated residual current per KW in KiloAmpere (for dynamic limit)	W/A
30180	float	RD/WR	Offset for residual current limit in Ampere (for dynamic limit)	A
30182	float	RD/WR	Early warning in relation to rcm limit in percent	%
30184	float	RD/WR	Value of threshold in Ampere for power Stage 0	A
30186	float	RD/WR	Value of threshold in Ampere for power Stage 1	A
30188	float	RD/WR	Value of threshold in Ampere for power Stage 2	A
30190	float	RD/WR	Value of threshold in Ampere for power Stage 3	A
30192	float	RD/WR	Value of threshold in Ampere for power Stage 4	A
30194	float	RD/WR	Value of threshold in Ampere for power Stage 5	A
30196	float	RD/WR	Value of threshold in Ampere for power Stage 6	A
30198	float	RD/WR	Value of threshold in Ampere for power Stage 7	A
30200	float	RD/WR	Value of threshold in Ampere for power Stage 8	A
30202	float	RD/WR	Value of threshold in Ampere for power Stage 9	A
30204	float	RD/WR	Value in Watts for power stage 0	W
30206	float	RD/WR	Value in Watts for power stage 1	W
30208	float	RD/WR	Value in Watts for power stage 2 Status byte description	W
30210	float	RD/WR	Value in Watts for power stage 3	W
30212	float	RD/WR	Value in Watts for power stage 4	W

Address	Format	RD/WR	Designation1	Designation2
30214	float	RD/WR	Value in Watts for power stage 5	W
30216	float	RD/WR	Value in Watts for power stage 6	W
30218	float	RD/WR	Value in Watts for power stage 7	W
30220	float	RD/WR	Value in Watts for power stage 8	W
30222	float	RD/WR	Value in Watts for power stage 9	W
30224	float	RD/WR	RCM static limit in Ampere	A
30226	float	RD/WR	Minimum exceeding time in seconds	s
30228-30249	-	-	-	Reserve
30250	short	RD/WR	TCP Mode: 0: fixed IP 1: DHCP-Client	
30251-30252	uint32	RD/WR	IP[4]	
30253-30254	uint32	RD/WR	Mask[4]	
30255-30256	uint32	RD/WR	Gate[4]	
30260	uint32	RD/WR	Max. Min. Period Time	0: History Max. Min. Value !0: Zone Max. Min. Value 0-9999 min.
30261	short	RD/WR	Average value period time	1-9999 s
30262	short	RD/WR	Sample value period time	1-9999 s
30263	short	WR	Reset Energy	1: Reset All Energy !(1): Null
30264	short	WR	Reset Demand	1: Reset Demand !(1): Null
30265	short	WR	Reset Max/Min Value	1: Reset !(1): Null
30266	shor	WR	Reset System Record	1: Reset !(1): Null
30267	short	WR	Reset SOE Event	1: Reset !(1): Null
30268	short	WR	Reset Recordings	1: Reset !(1): Null
30269	short	WR	Reset RCM Status	1: Reset !(1): Null
30270-30279	UTF-8	RD/WR		Device Name
30280-30299	UTF-8	RD/WR		Device Description

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